

16550A Performance Characteristics	
Form Factor:	Module (proprietary interface/bus)
Number of Channels:	102 ch
Maximum Conventional Timing Rate:	250 MHz
Maximum Conventional Timing Rate(half ch):	500 MHz
Memory Depth/Channel-Normal:	4 KB
Channel to Channel Delay:	2 ns
Minimum Recognizable Glitch Width:	3.5 ns
Maximum State Analysis Rate:	100 MHz
Setup/Hold Time:	3.5 ns
Maximum Trigger Sequence Levels:	12 Levels
Maximum Trigger Sequencing Rate:	125 MHz
No of Range Recognizers:	2 QTY
No of Counter Timers:	2 QTY
Input Impedance:	100 kOhm
Input Capacitance:	8 pF

